



IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Kumi MIYACHI et al. Conf.: 3629

Appl. No.: 09/615,956 Group: 2133

Filed: July 13, 2000 Examiner: Decady

For: SEMICONDUCTOR DEVICE HAVING INTEGRALLY
SEALED INTEGRATED CIRCUIT CHIPS
ARRANGED FOR IMPROVED TESTING

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DEC 03 2002

Technology Center 2100

LARGE ENTITY TRANSMITTAL FORM

Assistant Commissioner for Patents
Washington, DC 20231

November 27, 2002

Sir:

Transmitted herewith is an amendment in the above-identified application.

- ☐ The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
- ☐ The enclosed document is being transmitted via facsimile.

The fee has been calculated as shown below:

	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR		PRESENT EXTRA	RATE	ADDITIONAL FEE
TOTAL	14	-	20	=	0	\$18	\$0.00
INDEPENDENT	10	-	5	=	5	\$84	\$420.00
<input type="checkbox"/> FIRST PRESENTATION OF A MULTIPLE DEPENDENT CLAIM						\$280	\$0.00
						TOTAL	\$420.00

- ☐ Petition for () month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
- ☐ No fee is required.
- ☒ Check(s) in the amount of \$420.00 is(are) enclosed.
- ☐ Please charge Deposit Account No. 02-2448 in the amount of \$0.00. This form is submitted in triplicate.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. § 1.16 or under 37 C.F.R. § 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

By 
Charles Gorenstein, #29,271

RWD
CG/RWD/mag
1248-0509P

P.O. Box 747
Falls Church, VA 22040-0747
(703) 205-8000

Attachment(s)

(Rev. 10/15/02)



#6A/12-10-02
V. Jones

PATENT
1248-0509P

IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant: Kumi MIYACHI et al. Confirmation No.: 3629

Appl. No.: 09/615,956 Group: 2133

Filed: July 13, 2000 Examiner: Decady

For: SEMICONDUCTOR DEVICE HAVING INTEGRALLY SEALED
INTEGRATED CIRCUIT CHIPS ARRANGED FOR
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AMENDMENT

Assistant Commissioner of Patents
and Trademarks

Technology Center 2100
November 27, 2002

Sir:

Responsive to the Office Action dated August 27, 2002, the following amendments and remarks are respectfully submitted in connection with the above-identified application.

IN THE TITLE:

Please replace the title with the following new title.

--SEMICONDUCTOR DEVICE HAVING INTEGRALLY SEALED
INTEGRATED CIRCUIT CHIPS ARRANGED FOR IMPROVED TESTING--

IN THE CLAIMS

Please add the following new claims:

--8. (New) A semiconductor device, comprising:

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420.00 OP